

[illegible]

ESP8266 Module

The diagram illustrates the pin connections and power regulation for an ESP8266 module. The top part shows the module's pinout with RX, TX, ESP_RESET, and TX pins connected to a +3.3V supply and GND. The bottom part shows a power regulation circuit using an LD1117 voltage regulator, with input from a POWER source and output to a +3.3V supply. The circuit includes several capacitors (C2, C3, C4, C5) for filtering and decoupling.

ESP8266 Pin Connections:

- Pin 2 (RX) is connected to +3.3V.
- Pin 3 (ESP_RESET) is connected to +3.3V.
- Pin 4 is connected to GND.
- Pin 5 is connected to +3.3V.
- Pin 6 is connected to GND.
- Pin 7 (TX) is connected to +3.3V.
- Pin 8 is connected to GND.

Power Regulation Circuit:

- The circuit uses an LD1117 voltage regulator (IC2).
- Input (P2) is connected to the POWER source.
- Output (P3) is connected to the +3.3V supply.
- Capacitors C2 (100nF) and C3 (10uF) are connected between the input and ground.
- Capacitors C4 (22uF) and C5 (100nF) are connected between the output and ground.

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